

Fig  
9/22/00

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	47019	TFT or (thin adj film adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
2	BRS	L2	245734	LCD or (liquid adj crystal adj display)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
3	BRS	L3	30167	flicker\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
4	BRS	L4	1610	1 and 2 and 3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
5	BRS	L7	1830	recharge\$4 and (low adj temperature)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
6	BRS	L8	3	4 and 7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
7	BRS	L9	118	2 and 7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
8	BRS	L10	4	3 and 9	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB

	Issue Date	Pages	Title	Document ID	Current OR
1	20020815	9	Device for eliminating the flickering phenomenon of TFT-LCD	US 20020109657 A1	345/92
2	20010828	97	Voltage generating apparatus	US 6281826 B1	341/150
3	19750923	43	Liquid crystal display system	US 3907405 A	345/101
			Thin film transistor liquid crystal display flickering reduction device has trigger signal source to control conduction of transistors and thereby control recharging and discharging of discharge circuit	US 20020109657 A	
4	20020815	9			

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3	19980825	23	Liquid crystal display apparatus	US 5798744 A	345/92

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8	BRS	L10	4	3 and 9	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
9	BRS	L11	16913	switch and (low adj temperature)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
10	BRS	L12	100	9 and 11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
11	BRS	L13	598	345/55.cc1s.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
12	BRS	L14	3920	(345/87-90).cc1s.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB

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13	BRS	L15	2132	(345/92-96) .cc1s.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
14	BRS	L16	207	(349/39) .cc1s.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
15	BRS	L17	0	(349/204) .cc1s.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
16	BRS	L18	6392	13 or 14 or 15 or 16 or 17	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
17	BRS	L19	3	12 and 18	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB

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17	BRS	L19	3	12 and 18	USPAT; US_PGPUB; EPO; JPO; DERWENT; IBM_TDB
18	BRS	L20	460	discharged adj circuit	USPAT; US_PGPUB; EPO; JPO; DERWENT; IBM_TDB
19	BRS	L21	62804	open\$4 near switch	USPAT; US_PGPUB; EPO; JPO; DERWENT; IBM_TDB
20	BRS	L22	664661	delay\$4	USPAT; US_PGPUB; EPO; JPO; DERWENT; IBM_TDB
21	BRS	L23	18	20 and 21 and 22	USPAT; US_PGPUB; EPO; JPO; DERWENT; IBM_TDB
22	BRS	L24	8	thermistor and (delayed adj opening) and switch	USPAT; US_PGPUB; EPO; JPO; DERWENT; IBM_TDB
23	BRS	L25	0	4 and 24	USPAT; US_PGPUB; EPO; JPO; DERWENT; IBM_TDB